| | Application No. | Applicant(s) |
|---|---|---|
| | 09/893,616 | GREEFF ET AL. |
| Notice of Allowability | Examiner | Art Unit |
| | Raymond Phan | 2111 |
| The MAILING DATE of this communication appear All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI | (OR REMAINS) CLOSED in this apport or other appropriate communication IGHTS. This application is subject to | plication. If not included will be mailed in due course. THIS |
| 1. This communication is responsive to the amendment filed on August 19, 2004. | | |
| 2. A The allowed claim(s) is/are 1,3-45,47-98,100-105,107-110 and 112-127. | | |
| 3. The drawings filed on 29 Jun 2001 are accepted by the Examiner. | | |
| 4. Acknowledgment is made of a claim for foreign priority ur a) All b) Some* c) None of the: 1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 3. Copies of the certified copies of the priority documents have International Bureau (PCT Rule 17.2(a)). * Certified copies not received: | been received. been received in Application No | |
| Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE. | | complying with the requirements |
| 5. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give | | |
| 6. CORRECTED DRAWINGS (as "replacement sheets") mus | st be submitted. | |
| (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached | | |
| 1) hereto or 2) to Paper No./Mail Date | | |
| (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date | | |
| Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in t | .84(c)) should be written on the drawing the header according to 37 CFR 1.121(| ngs in the front (not the back) of d). |
| 7. DEPOSIT OF and/or INFORMATION about the depo attached Examiner's comment regarding REQUIREMENT | | |
| • | | |
| Attachment(s) | 5 D Notice of Informal B | atent Application (PTO-152) |
| Notice of References Cited (PTO-892) ■ Dotice of Draftperson's Patent Drawing Review (PTO-948) | 6. ☐ Interview Summary | ,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,, |
| | _ Paper No./Mail Dat | te |
| Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date | 08), 7. ☐ Examiner's Amendr | ment/Comment |
| 4. Examiner's Comment Regarding Requirement for Deposit | | ent of Reasons for Allowance |
| of Biological Material | 9. | |
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Allowable Subject Matter

- 1. Claims 1, 3-45, 47-98, 100-105, 107-110, 112-127 are allowable over the prior art of records.
- The instant application is deemed to be directed to an unobvious 2. improvement over the inventions patented in Pat. No. 6,662,260 and 6,338,107. The improvements comprise a a second switching circuit connected between said second segment of said data bus and a third segment of said data bus, wherein said second switching circuit is configured to selectively connect said second and third segments of said data bus such that when said second switching circuit is in a first state, said second switching circuit passes data through from said second bus segment to said third bus segment and from said third bus segment to said second bus segment, and when said second switching circuit is in a second state, said third bus segment is disconnected from said second bus segment and data is passed through from said second bus segment to at least one I/O circuit and from said at least one I/O circuit to said second bus segment (claim 1); wherein said first switching circuit receives a command selecting at least one attached I/O circuit for point-to-point communications, selects said second state of said first switching circuit to disconnect said first bus segment from said second bus segment, and passes data between said first bus segment and said at least one attached selected I/O circuit (claims 17, 36, 64-65, 101, 110); wherein said data bus is a first data bus having a first number of data paths, and said first switching circuit is further configured to connect to a second data bus having a second number of data paths, wherein said first switching circuit is connected between said first and second data buses for selectively receiving data on said first data bus and placing said data on said second data bus and selectively receiving data on said second data bus and

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placing said data on said first data bus (claims 18, 47); wherein a switchable terminator is included in at least one of said plurality of system devices (claim 90); wherein said selective passing of data includes configuring said at least one switching circuit to pass data during WRITE operations (claim 107); wherein said selective passing of data includes configuring said at least one switching circuit to pass data between a memory controller and a selected I/O device during READ operations (claim 108); wherein said selective passing of data includes configuring said at least one switching circuit to pass data between a memory controller and a selected I/O device during WRITE operations (claim 109).

3. The reason for allowance of claims 112-127 can be found in previous Office Action.

The remaining claims, not specifically mentioned, are allowed for the same rationale from the parent claim by dependency.

- 4 The title of invention has been amended as follows:
- -- Data transmission circuit for memory subsystem, has switching circuit that selectively connects or disconnects two data bus segments to respectively enable data transmission or I/O circuit connection --
- 5. Any comments considered necessary by applicant must be submitted no later than the payment of the Issue Fee and, to avoid processing delays, should preferable **accompany** the Issue Fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

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6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to examiner Raymond Phan, whose telephone number is (571) 272-3630. The examiner can normally be reached on Monday-Friday from 7:30AM-4:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's Primary, Paul Myers can be reached on (571) 272-3639 or via e-mail addressed to paul.myers@uspto.gov. The fax phone number for this Group is (703) 746-7239.

Communications via Internet e-mail regarding this application, other than those under 35 U.S.C. 132 or which otherwise require a signature, may be used by the applicant and should be addressed to [raymond.phan@uspto.gov].

All Internet e-mail communications will be made of record in the application file. PTO employees do not engage in Internet communications where there exists a possibility that sensitive information could be identified or exchanged unless the record includes a properly signed express waiver of the confidentiality requirements of 35 U.S.C. 122. This is more clearly set forth in the Interim Internet Usage Policy published in the Official Gazette of the Patent and Trademark on February 25, 1997 at 1195 OG 89.

Any inquiry of a general nature or relating to the status of this application should be directed to the TC 2100 central telephone number is (571) 272-2100.

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PAUL R. MYERS
PRIMARY EXAMINER

Paul R. Myon

Raymond Phan October 23, 2004